



## Xilinx Introduces HDMI 2.1 IP Subsystem

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### 8K UHD video support on Xilinx enables next-generation video capture, display, processing and machine learning

AMSTERDAM, Feb. 5, 2019 /PRNewswire/ -- Xilinx, Inc. (NASDAQ: XLNX), the leader in adaptive and intelligent computing, today announced that it has introduced a complete HDMI™ 2.1 IP subsystem to its portfolio of intellectual property cores, enabling Xilinx® devices to transmit, receive and process up to 8K (7680 x 4320 pixels) ultra-high-definition (UHD) video in pro AV equipment, including cameras, streaming media players, professional monitors, LED walls, projectors and KVM, as well as broadcast products such as end points and infrastructure that are being upgraded to handle 8K video.



Customers are increasingly adopting machine learning to monetize and improve workflows in diverse applications such as broadcast, pro AV, automotive and surveillance. HDMI 2.1 data rates are provided by Xilinx's highly reliable high-speed I/O transceivers. This, combined with native 8K interfaces supported by HDMI 2.1 now make it possible to replace several ASSP's or fixed-function products support processing, compression, high-quality analytics and decision-making with a single Xilinx device.

"Today's professional AV and broadcast markets continue to demand higher resolution, higher frame rates and high dynamic range, to deliver more immersive viewing experiences," said Ramesh Iyer, director of marketing, Pro AV and Broadcast market, Xilinx. "Our customers can now implement the complete HDMI 2.1 interface on-chip, creating highly-integrated designs that can natively handle 8K processing. And customers can combine this functionality with 8K lightweight mezzanine codecs for 8K over IP streaming, reducing real-estate, power consumption and BOM costs."

Xilinx will demonstrate the HDMI 2.1 IP subsystem alongside a range of other cores for compression, video processing and digital-signage analytics at booth 15-D240, ISE Integrated Systems Europe, RAI Amsterdam, February 5-8.

#### About Xilinx

Xilinx develops highly flexible and adaptive processing platforms that enable rapid innovation across a variety of technologies – from the endpoint to the edge to the cloud. Xilinx is the inventor of the FPGA, hardware programmable SoCs and the ACAP, designed to deliver the most dynamic processor technology in the industry and enable the adaptable, intelligent and connected world of the future. For more information, visit [www.xilinx.com](http://www.xilinx.com).

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