



Xilinx Posts Record Sales of \$513 Million

[Financial Tables](#)

SAN JOSE, Calif., Jan 20, 2010 /PRNewswire via COMTEX/ -- Xilinx, Inc. (Nasdaq: XLNX) today announced record third quarter fiscal 2010 sales of \$513.3 million, up 24% sequentially and up 12% from the third quarter of the prior fiscal year. Third quarter net income was \$106.9 million, or \$0.38 per diluted share, including previously announced restructuring charges totaling \$5.5 million, or approximately \$0.02 per diluted share. On a year over year basis, third quarter net income and diluted earnings per share declined because in the third quarter of fiscal 2009 there was a \$58.3 million pre-tax gain from the early extinguishment of convertible debentures, partially offset by a \$19.5 million pre-tax impairment charge on investments.

(Logo: <http://www.newscom.com/cgi-bin/prnh/20020822/XLNXLOGO>)

The Xilinx Board of Directors announced a quarterly cash dividend of \$0.16 per outstanding share of common stock, payable on March 3, 2010 to all stockholders of record at the close of business on February 10, 2010.

Additional third quarter comparisons are represented in the charts below:



Record sales were driven by strength from all end markets and geographic regions. New Product sales increased 32% sequentially during the quarter due primarily to strong growth from the Virtex[®]-5 and Spartan[®]-3 FPGA families.

"It was a record-setting quarter for Xilinx," said Moshe Gavrielov, Xilinx President and Chief Executive Officer. "In addition to record total sales, Xilinx's Virtex-5 family comfortably surpassed \$100 million in quarterly sales--more than any other FPGA product in history. Sales strength from this family was driven by Communications, Industrial and Other and Data Processing. Based on new design win activity, I am confident about the prospects for continued sales growth from the Virtex-5 family."



Product and Financial Highlights

- Building upon Virtex-5 success, Xilinx today announced that its Virtex-6 family has achieved full production qualification and is commencing volume shipments. Xilinx also recently announced first shipments and availability of the industry's largest FPGA, the 40-nm LX760, which is ideal for use in advanced prototyping platforms and the ASIC emulation market.
- On October 19, 2009, Xilinx and ARM announced they are collaborating to enable ARM processor and interconnect technology on Xilinx FPGA products. This agreement is expected to allow programmable solutions to penetrate even deeper into existing markets and expand into new market spaces by leveraging ARM's processor strengths and ecosystem.
- On December 8, 2009, Xilinx announced six new Targeted Design Platform development kits for the Virtex-6 and Spartan-6 product families. The new kits increase productivity and innovation for electronic designers in the areas of target embedded processing, DSP and high speed serial connectivity.
- December quarter operating margin was 27%, up from 20% in the prior quarter and the highest reported by the Company in four years. Improved customer demand and continued spending discipline contributed to the operating margin increase.

Business Outlook - March Quarter Fiscal 2010

- Sales are expected to be up 3% sequentially to down 1% sequentially.
- Gross margin is expected to be approximately 64% to 65%.
- Operating expenses are expected to be approximately \$180 million, including \$3 million in previously announced restructuring charges.
- Other income and expense is expected to be a net expense of approximately \$2 million.
- Fully diluted share count is expected to be approximately 275 million.
- March quarter tax rate is expected to be approximately 20%.

Conference Call

A conference call will be held today at 2:00 p.m. Pacific Time to discuss the quarter's results and management's outlook for the March quarter. The webcast and subsequent replay will be available in the investor relations section of the company's web site at www.investor.xilinx.com. A telephonic replay of the call may be accessed later in the day by calling (800) 642-1687 and referencing confirmation code 47787165. The telephonic replay will be available for two weeks following the live call.

This release contains forward-looking statements and projections. Forward-looking statements and projections can often be identified by the use of forward-looking words such as "expect," "may," "will," "could," "believe," "anticipate," "estimate," "continue," "plan," "intend," "project" or other similar words. Undue reliance should not be placed on such forward-looking statements and projections, which speak only as of the date they are made. We undertake no obligation to update such forward-looking statements. Actual events and results may differ materially from those in the forward-looking statements and are subject to risks and uncertainties including the general health of global economies as well as of the semiconductor industry, the health of our end markets and our customers' customers, the ability of our foundry suppliers to deliver sufficient wafer volumes in a timely manner, our ability to forecast end customer demand, customer acceptance of our new products, the ability of the Company to generate cost and operating expense savings in an efficient and timely fashion, the ability of our customers to manage their inventories, a high dependence on turns business, more customer volume discounts than expected, greater product mix changes than anticipated, fluctuations in manufacturing yields, our ability to deliver product in a timely manner, our ability to successfully manage production at multiple foundries, variability in wafer pricing, and other risk factors listed in our most recent Forms 10-K and 10-Q.

About Xilinx

Xilinx, Inc. (NASDAQ: XLNX) is the worldwide leader of programmable logic solutions. Additional information about Xilinx is available at <http://www.xilinx.com>.

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